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| U.S. Department of Commerce, Patent and Trademark Office | | | | | | Atty Docket No. | | Serial No. | |
| | | | | | | M-8502 US | | 09/754,903 | |
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT | | | | | | Applicant | | | |
| (Use several sheets if necessary) | | | | | | William J. Price | | | |
| | | | | | | Filing Date | | Group | |
| | | | | | | January 4, 2001 | | Unknown | |
| U.S. Patent Documents | | | | | | | | | |
| *Examiner Initial | | Document Number | Date | Name | Class | Subclass | Filing Date If Appropriate | | |
| | AA | | / | | | | | | |
| | AB | | | | | | | | |
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| | AD | | | | | | | | |
| Foreign Patent Documents | | | | | | | | | |
| | | | | | | | | Technology Center 2100 | |
| | | | | | | | | Translation | |
| | | Document | Date | Country | Class | Subclass | Yes | No | |
| | AE | | / | | | | | | |
| | AF | | | | | | | | |
| | AG | | | | | | | | |
| | AH | | | | | | | | |
| OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) | | | | | | | | | |
| RP | AI | "2-wire Serial EEPROM", downloaded 10/17/2000 from < http://www.atmel.com/atmel/psds/first_page/doc0180a.gif >, Atmel® (2000). | | | | | | | |
| RP | AJ | "2x5 SFF Transceiver Modules", Finisar (2000). | | | | | | | |
| RP | AK | "LM75 Digital Temperature Sensor and Thermal watchdog with Two-Wire Interface", National Semiconductor Corporation (2000). | | | | | | | |
| RP | AL | "PCF8574 Remote 8-bit I/O expander for I ² C-bus" Data Sheet, Philips Semiconductors (1997). | | | | | | | |
| RP | AM | "SN74CBT3253 Dual 1-Of-4 FET Multiplexer/Demultiplexer", Texas Instruments (2000). | | | | | | | |
| RP | AN | "SN74HC4066 Quadruple Bilateral Analog Switch", Texas Instruments (1997). | | | | | | | |
| RP | AO | "SN74AHC1G04 Single Inverter Gate", Texas Instruments (2000). | | | | | | | |
| Examiner | | RAYMOND PHAN | | Date Considered | | 6-27-03 | | | |
| *EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant. | | | | | | | | | |

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| Foreign Patent Documents | | | | | Technology Center 2100 | | | |
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| | | Document | Date | Country | Class | Subclass | Yes | No |
| | AE | | / | | | | | |
| | AF | | | | | | | |
| | AG | | | | | | | |
| | AH | | | | | | | |
| OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) | | | | | | | | |
| RP | AI | "Advanced Product Information VSC7127/VSC7129" Vitesse Semiconductor Corporation (2000). | | | | | | |
| RP | AJ | "Advanced Product Information VSC7128", Vitesse Semiconductor Corporation (1998). | | | | | | |
| RP | AK | "Advanced Product Information VSC7130", Vitesse Semiconductor Corporation (2000). | | | | | | |
| RP | AL | "Preliminary Data Sheet VSC7132", Vitesse Semiconductor Corporation (2000). | | | | | | |
| RP | AM | "SSC100 FC-ALE Embedded Controller Product Brief" Vitesse Semiconductor Corporation (2000). | | | | | | |
| | AN | / | | | | | | |
| | AO | | | | | | | |
| Examiner <u>RAYMOND PHAN</u> | | Date Considered <u>6-27-03</u> | | | | | | |
| <p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p> | | | | | | | | |